

#### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V <sub>DS</sub>	-60	- V	
Gate-Source Voltage		V <sub>GS</sub>	±20		
	Tc=25°C	I <sub>D</sub>	-38		
Continuous Drain Current <sup>(Note 3)</sup>	Tc=100°C		-27	A	
Pulsed Drain Current <sup>(Note 1)</sup>	$T_c=25^{\circ}C$	I <sub>DM</sub>	-80		
Power Dissipation	$T_c=25^{\circ}C$	·Divi	75	w	
	Tc=100°C	Po	38		
Continuous Drain Current <sup>(Note 4)</sup>	T <sub>A</sub> =25°C		-6.7		
	T <sub>A</sub> =70°C	I <sub>D</sub>	-6	A	
Power Dissipation	T <sub>A</sub> =25 <sup>°</sup> C	Po	2.5	14/	
	T <sub>A</sub> =70 <sup>°</sup> C		1.8	W	
Single Pulse Avalanche Current <sup>(Note 5)</sup>		las	-14	А	
Single Pulse Avalanche Energy <sup>(Note 5)</sup>		Eas	98	mJ	
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~175	°C	
Thermal Resistance <sup>(Note 4)</sup>	Junction to Case	$R_{ extsf{ heta}JC}$	2	- °C/W	
	Junction to Ambient	$R_{ heta JA}$	60		



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#### Electrical Characteristics (TA=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static		-					
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-60	-	-	V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-1	-1.7	-2.5		
		V <sub>G</sub> s=-10V, I <sub>D</sub> =-20A	-	22.3	28		
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-10A	-	29.5	38	mΩ	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0V	-	-	-1	uA	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA	
Dynamic <sup>(Note 6)</sup>							
Total Gate Charge	Qg	V <sub>DS</sub> =-30V, I <sub>D</sub> =-20A,	-	44	58	nC	
Gate-Source Charge	Qgs		-	11	-		
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =-10V	-	7	-		
Input Capacitance	Ciss	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V,	-	2530	3290	pF	
Output Capacitance	Coss		-	151	228		
Reverse Transfer Capacitance	Crss	f=1MHz	-	108	162		
Gate resistance	Rg	f=1MHz	-	4.8	-	Ω	
Turn-On Delay Time	td(on)	V <sub>DS</sub> =-30V, I <sub>D</sub> =-20A, V <sub>GS</sub> =-10V, R <sub>G</sub> =3Ω	-	7	-	ns	
Turn-On Rise Time	tr		-	9	-		
Turn-Off Delay Time	td <sub>(off)</sub>		-	41	-		
Turn-Off Fall Time	tf		-	21	-		
Drain-Source Diode							
Diode Forward Current	I <sub>S</sub>	T 05º0	-	-	-38	A	
Pulsed Diode Forward Current	I <sub>SM</sub>	T <sub>C</sub> =25°C	-	-	-80		
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-20A, V <sub>GS</sub> =0V	-	-0.85	-1.3	V	
Reverse Recovery Time	Trr	V <sub>DD</sub> =-30V,V <sub>GS</sub> =0V	-	16	-	ns	
Reverse Recovery Charge	Qrr	Is=-20A,dIs/dt=100A/us	-	10	-	nC	

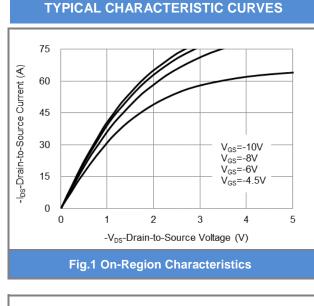
NOTES :

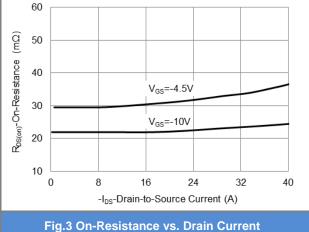
- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. The maximum current rating is package limited.
- 4.  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
- 5. E<sub>AS</sub> is calculated based on the condition of L=1mH, I<sub>AS</sub>=-14A, V<sub>DD</sub>=-30V, V<sub>GS</sub>=-10V. 100% test at L=0.5mH, I<sub>AS</sub>=-14A in production.
- 6. Guaranteed by design, not subject to production testing.

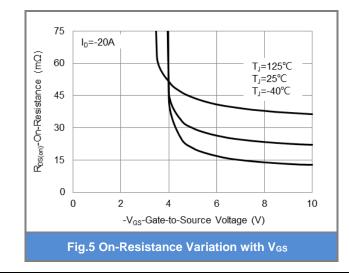
SEMI CONDUCTOR

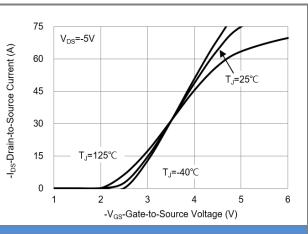
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#### **Fig.2 Transfer Characteristics**

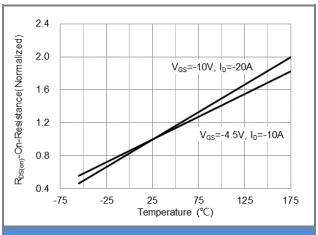
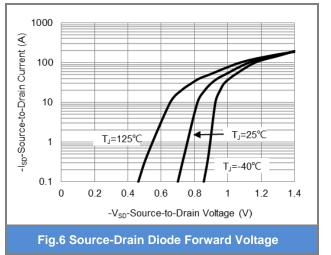


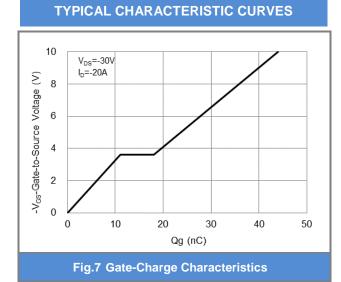
Fig.4 On-Resistance vs. Junction temperature

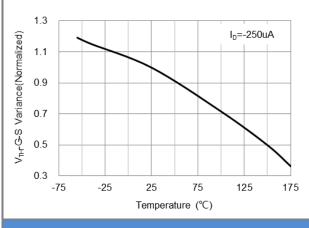


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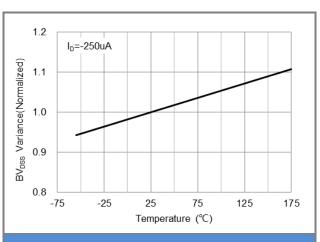
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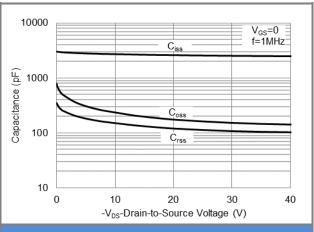
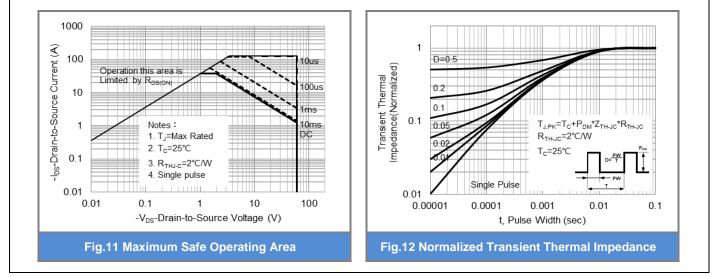


Fig.10 Capacitance vs. Drain-Source Voltage



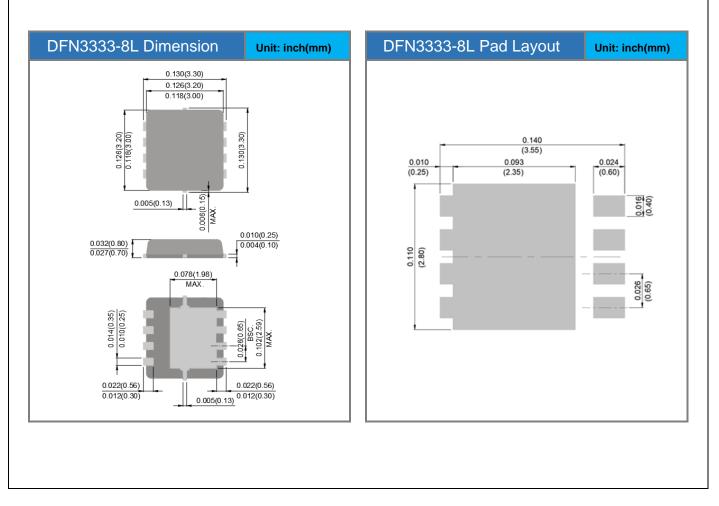


# PJQ44605AP-AU

#### **Product and Packing Information**

Part No.	Package Type	Packing Type	Marking	
PJQ44605AP-AU	DFN3333-8L	5K pcs / 13" reel	44605A	

#### Packaging Information & Mounting Pad Layout





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