

### 150V N-Channel Enhancement Mode MOSFET

Voltage 150 V Current 9 A

#### **Features**

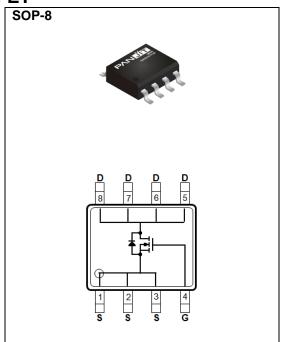
- RDS(ON), VGS@10V, ID@9A< $54m\Omega$
- RDS(ON), VGS@7V, ID@5A<59m $\Omega$
- Excellent FOM
- Standard Level Drive
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### **Mechanical Data**

• Case: SOP-8 Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.083 grams



## **Maximum Ratings and Thermal Characteristics** (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		$V_{DS}$	150	V	
Gate-Source Voltage		$V_{GS}$	±20	v	
Continuous Drain Current(Note 3)	T <sub>C</sub> =25°C		9		
	T <sub>C</sub> =100°C	l <sub>D</sub>	5.6	А	
Pulsed Drain Current(Note 1)	Tc=25°C	I <sub>DM</sub>	36		
Power Dissipation	Tc=25°C	D-	10.4	W	
	T <sub>C</sub> =100°C	Po	4.2		
Continuous Drain Current(Note 4)	T <sub>A</sub> =25°C		3.9	А	
	T <sub>A</sub> =70°C	I <sub>D</sub>	3.1		
Power Dissipation	T <sub>A</sub> =25°C	PD	2.1	W	
	T <sub>A</sub> =70°C		1.3		
Single Pulse Avalanche Current(Note 5)		las	10	А	
Single Pulse Avalanche Energy <sup>(Note 5)</sup>		E <sub>AS</sub>	32	mJ	
Operating Junction and Storage Temperature Range		T <sub>J</sub> ,T <sub>STG</sub>	-55~150	°C	
Thermal Resistance <sup>(Note 4)</sup>	Junction to Case	R <sub>θJC</sub>	12	°C/W	
	Junction to Ambient	R <sub>θJA</sub>	60		



### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	150	-	-	- V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	2	3	4	V	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =9A	1	43	54		
		V <sub>GS</sub> =7V, I <sub>D</sub> =5A	=5A - 45 59		59	mΩ	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =150V, V <sub>GS</sub> =0V	ı	-	1	uA	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA	
Dynamic <sup>(Note 6)</sup>						_	
Total Gate Charge	$Q_g$	V <sub>DS</sub> =75V, I <sub>D</sub> =9A,	ı	22	29	nC	
Gate-Source Charge	Qgs		ı	7	-		
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =10V	-	6	-		
Input Capacitance	Ciss		-	1116	1450	pF	
Output Capacitance	Coss	V <sub>DS</sub> =75V, V <sub>GS</sub> =0V,	-	81	142		
Reverse Transfer Capacitance	Crss	f=1MHz	-	23	-		
Gate resistance	Rg	f=1MHz	-	0.8	-	Ω	
Turn-On Delay Time	td <sub>(on)</sub>	\(\(\begin{array}{cccccccccccccccccccccccccccccccccccc	-	8.4	-	ns	
Turn-On Rise Time	tr	V <sub>DS</sub> =75V, I <sub>D</sub> =9A,	-	14	-		
Turn-Off Delay Time	td(off)	$V_{GS}=10V, R_{G}=3\Omega$ (Note 2)	-	17	-		
Turn-Off Fall Time	tf	(NOTE 2)	-	11	-		
Drain-Source Diode							
Diode Forward Current	Is	T 05°0	-	-	9	A	
Pulsed Diode Forward Current	I <sub>SM</sub>	T <sub>C</sub> =25°C	-	-	36		
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =10A, V <sub>GS</sub> =0V	-	0.9	1.3	V	
Reverse Recovery Time	Trr	V <sub>DD</sub> =75V,V <sub>GS</sub> =0V	-	58	-	ns	
Reverse Recovery Charge	Qrr	Is=20A,dIs/dt=100A/us	-	90	-	nC	

#### NOTES:

- 1. Pulse width<100us, Duty cycle<2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Chip capability with an ReJC=12°C/W.
- 4. R<sub>BJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
- 5.  $E_{AS}$  is calculated based on the condition of L=1mH,  $I_{AS}$ =8A,  $V_{DD}$ =30V,  $V_{GS}$ =10V. 100% test at L=0.1mH,  $I_{AS}$ =10A in production.
- 6. Guaranteed by design, not subject to production testing.



#### **TYPICAL CHARACTERISTIC CURVES**

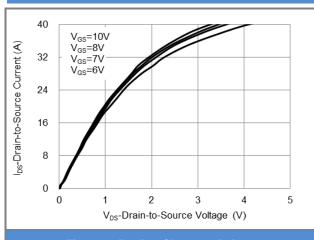
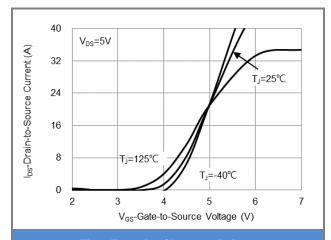


Fig.1 On-Region Characteristics



**Fig.2 Transfer Characteristics** 

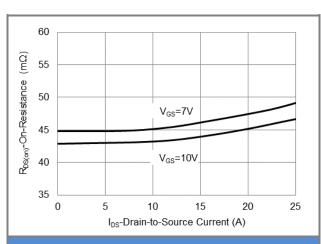


Fig.3 On-Resistance vs. Drain Current

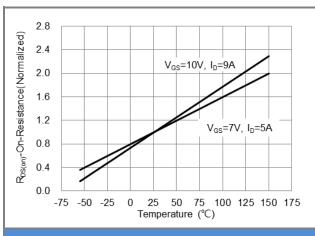
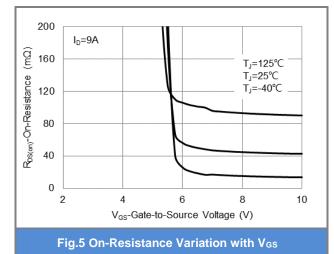


Fig.4 On-Resistance vs. Junction temperature



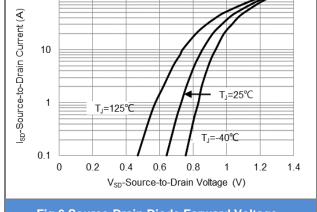


Fig.6 Source-Drain Diode Forward Voltage

100



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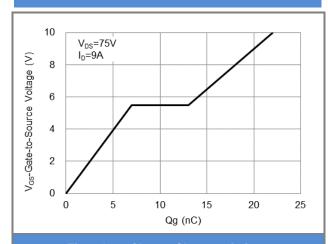


Fig.7 Gate-Charge Characteristics

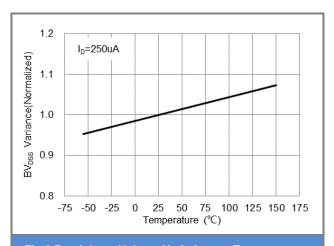


Fig.8 Breakdown Voltage Variation vs. Temperature

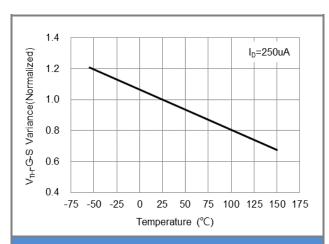


Fig.9 Threshold Voltage Variation with Temperature

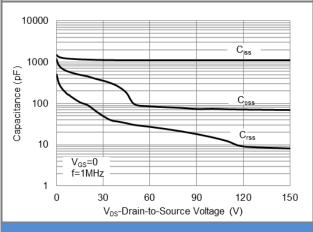
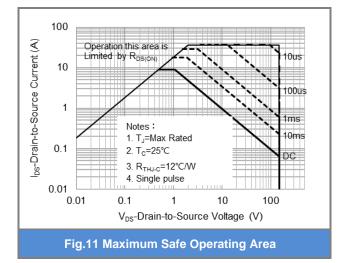


Fig.10 Capacitance vs. Drain-Source Voltage



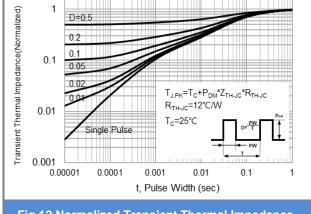


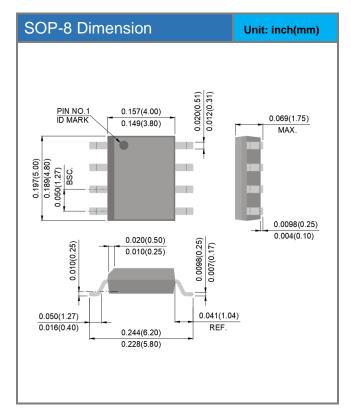
Fig.12 Normalized Transient Thermal Impedance

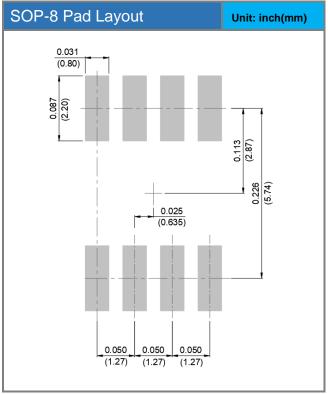


### **Product and Packing Information**

Part No.	Package Type	Package Type Packing Type	
PJL9580	SOP-8	2.5K pcs / 13" reel	L9580

### **Packaging Information & Mounting Pad Layout**







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