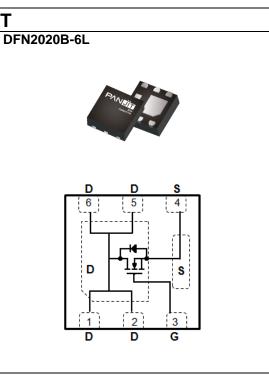
PJQ2566A	
	Г DF
	PJQ2566A 60V N-Channel Enhancement Mode MOSFE

Features

- Rds(on), Vgs@10V, Id@10A<14mΩ
- Rds(on), Vgs@4.5V, Id@6A<21m Ω
- Excellent FOM
- Logic Level Drive
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : DFN2020B-6L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0086 grams



Maximum Ratings and Thermal Characteristics (T_A=25^oC unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	60	- v
Gate-Source Voltage		V _{GS}	±20	v
Quality Durin Quarty (Note 2)	$T_c=25^{\circ}C$		20	
Continuous Drain Current ^(Note 3)	Tc=100°C	I _D	12.7	Α
Pulsed Drain Current ^(Note 1)	Tc=25°C	I _{DM}	80	1
Power Dissipation	Tc=25°C	PD	9.6	14/
	Tc=100°C		3.8	W
Continuous Drain Current ^(Note 4)	T _A =25 [°] C		8.4	А
	T _A =70 [°] C	ID	6.7	
Power Dissipation	T _A =25°C	Da	1.7	14/
	T _A =70 [°] C	PD	1.1	W
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~150	°C
Thermal Resistance ^(Note 4)	Junction to Case	R _{θJC}	13	°C/W
	Junction to Ambient	R _{θJA}	75	0/11



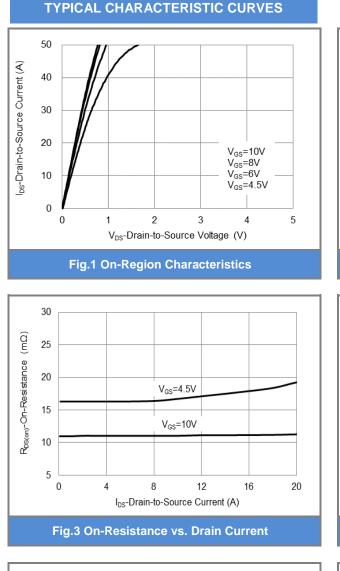
Electrical Characteristics (TA=25°C unless otherwise noted)

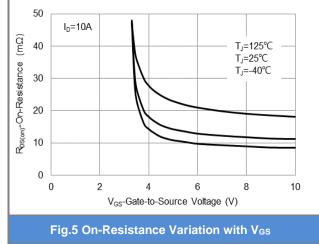
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV _{DSS}	BV _{DSS} V _{GS} =0V, I _D =250uA		-	-		
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	1	1.4	2.5	V	
		V _{GS} =10V, I _D =10A	-	11.2	14		
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =6A	-	16.3	21	mΩ	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V	-	-	1	uA	
Gate-Source Leakage Current	I _{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	±100	nA	
Dynamic ^(Note 6)	-						
Total Gate Charge	Qg		-	16.7	22		
Gate-Source Charge	Qgs	V _{DS} =30V, I _D =10A, V _{GS} =10V ^(Note 2,3)	-	3.3	-	nC	
Gate-Drain Charge	Q_{gd}	VGS=10V(1000 2,0)	-	4	-		
Input Capacitance	Ciss		-	834	1168		
Output Capacitance	Coss	V _{DS} =30V, V _{GS} =0V, f=1MHz	-	323	485	pF	
Reverse Transfer Capacitance	Crss	I=IMH2	-	18	-		
Gate resistance	Rg	f=1MHz	-	1.3	-	Ω	
Turn-On Delay Time	td(on)		-	7	-		
Turn-On Rise Time	tr	$V_{DS}=30V, I_{D}=10A,$	-	20	-		
Turn-Off Delay Time	td _(off)	V _{GS} =10V, R _G =3Ω (Note 2,3)	-	17	-	ns	
Turn-Off Fall Time	tf	(14016 2,3)	-	16	-		
Drain-Source Diode	·						
Diode Forward Current	I _S	T 0500	-	-	20	٨	
Pulsed Diode Forward Current	I _{SM}	T _C =25°C	-	-	80	A	
Diode Forward Voltage	V _{SD}	I _S =10A, V _{GS} =0V	-	0.85	1.3	V	
Reverse Recovery Time	Trr	V _{DD} =30V,V _{GS} =0V	-	40	-	ns	
Reverse Recovery Charge	Qrr	Is=10A,dIs/dt=100A/us	-	21	-	nC	

NOTES :

- 1. Pulse width
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Chip capability with an $R_{\theta JC}=13^{\circ}C/W$.
- 4. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 5. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=150$ °C. Ratings are based on low frequency and duty cycles to keep initial $T_J=25$ °C.
- 6. Guaranteed by design, not subject to production testing.







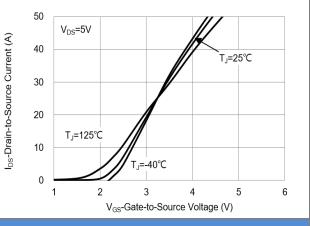


Fig.2 Transfer Characteristics

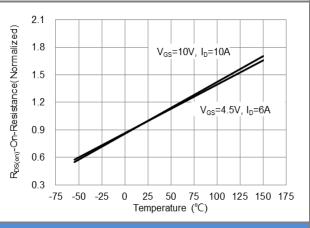
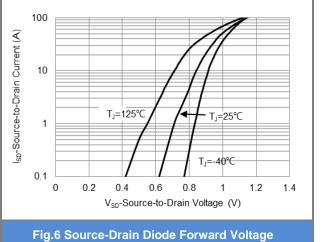
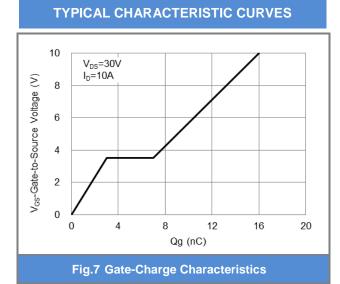
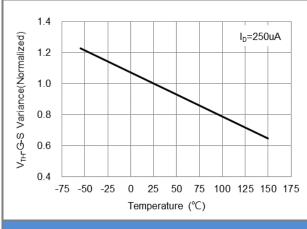


Fig.4 On-Resistance vs. Junction temperature

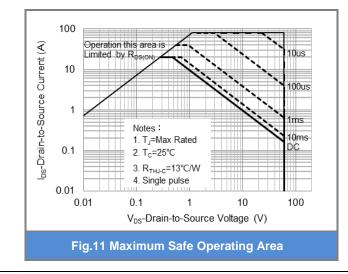


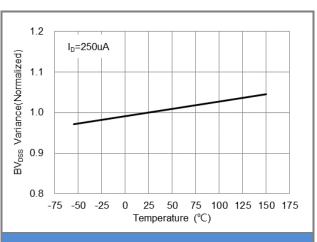




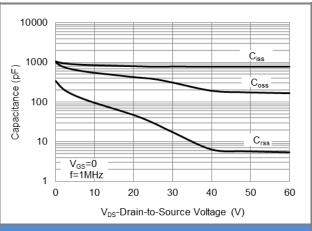


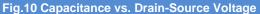


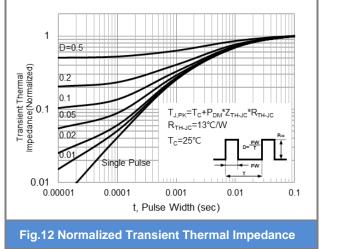










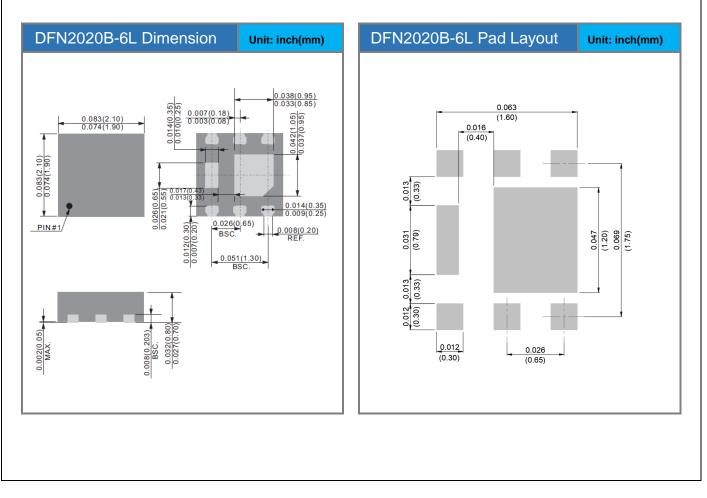




Product and Packing Information

Part No.	Part No. Package Type Packing Type		Marking
PJQ2566A	DFN2020B-6L	3K pcs / 7" reel	566

Packaging Information & Mounting Pad Layout





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