

Maximum Ratings (Q1) (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Collector-Base Voltage	V _{CBO}	60	V
Collector-Emitter Voltage	V _{CEO}	40	V
Emitter-Base Voltage	V _{EBO}	6	V
Collector Current (DC)	I _C	200	mA

OUT

BIAS

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Maximum Ratings (Q2) (T_A=25[°]C unless otherwise noted)

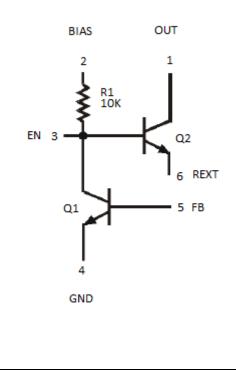
PARAMETER	SYMBOL	LIMIT	UNITS
Collector-Base Voltage	V _{CBO}	30	V
Collector-Emitter Voltage	V _{CEO}	30	V
Emitter-Base Voltage	V _{EBO}	5	V
Collector Current (DC)	Ι _c	150	mA

Thermal Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

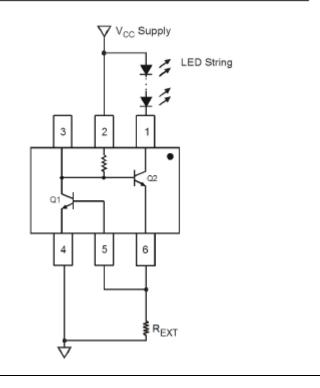
PARAMETER	SYMBOL	LIMIT	UNITS
Collector Power Dissipation (Note)	P _D	1	W
Operating Junction and Storage Temperature Range	T_{J},T_{STG}	-55~150	°C
Thermal Resistance from Junction to Ambient (Note)	$R_{ extsf{ heta}JA}$	125	°C/W

Note : Mounted on a 1 inch FR-4 with 2oz. square pad of copper.

Device Schematic



Applications Circuit







Electrical Characteristics (Q1) ($T_A=25^{\circ}C$ unless otherwise noted)

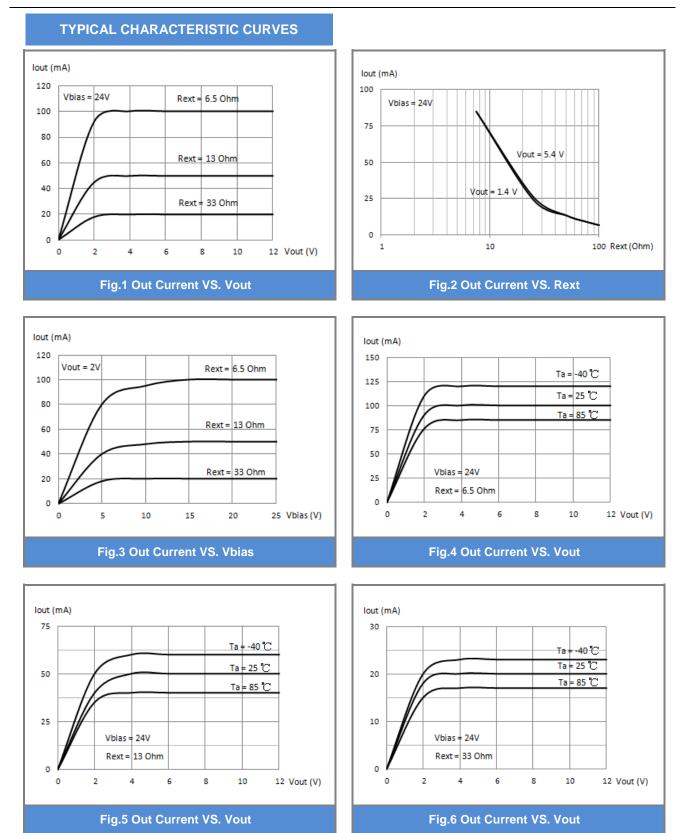
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
OFF Characteristics						
Collector-Emitter Breakdown Voltage	BV _{CEO}	I_{C} = 1mA, I_{B} = 0A	40	-	-	V
Collector-Base Breakdown Voltage	BV _{CBO}	I_{C} = 10uA, I_{E} = 0A	60	-	-	V
Emitter-Base Breakdown Voltage	BV _{EBO}	I _E = 10u A, I _C = 0A	6	-	-	V
Collector Cutoff Current	I _{CEX}	$V_{CE}=30V, V_{EB}=3V$	-	-	50	nA
Base Cutoff Current	I _{BL}	V_{CE} = 30V, V_{EB} = 3V	-	-	50	nA
ON characteristics						
	h _{FE}	V_{CE} = 1V, I _C = 0.1mA	40	-	-	-
		V_{CE} = 1V, I_{C} = 1mA	70	-	-	
DC Current Gain		V_{CE} = 1V, I _C = 10mA	100	-	300	
		V _{CE} = 1V, I _C = 50mA	60	-	-	
		V _{CE} = 1V, I _C = 100mA	30	-	-	
	V _{CE(SAT)}	I _C = 10mA, I _B = 1mA	-	-	200	mV
Collector-Emitter Saturation Voltage		I _C = 50mA, I _B = 5mA	-	-	300	
Base-Emitter Saturation Voltage	V _{BE(SAT)}	I _C = 10mA, I _B = 1mA	-	-	850	mV
		I _C = 50mA, I _B = 5mA	-	-	950	
Transition Frequency	f _T	V _{CE} = 20V, I _C = 10mA	300	-	-	MHz
Collector-Base Capacitance	C _{CBO}	V _{CB} = 5V, f= 1MHz	-	-	4	pF
Emitter-Base Capacitance	C _{EBO}	V _{EB} = 0.5V, f= 1MHz	-	-	8	pF





Electrical Characteristics (Q2) ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
OFF Characteristics						
Collector-Emitter Breakdown Voltage	BV_{CEO}	I _C = 1mA, I _B = 0A	30	-	-	V
Collector-Base Breakdown Voltage	BV _{CBO}	I _C = 50uA, I _E = 0A	30	-	-	V
Emitter-Base Breakdown Voltage	BV _{EBO}	I _E = 50u A, I _C = 0A	5	-	-	V
Collector Cutoff Current	I _{CBO}	V_{CB} = 30V, I_{E} = 0A	-	-	0.5	uA
Emitter Cutoff Current	I _{EBO}	V _{EB} = 4V	-	-	0.5	uA
ON characteristics						
DC Current Gain	h _{FE}	V _{CE} = 5V, I _C = 150mA	100	-	-	-
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	I _C = 10mA, I _B = 1mA	-	-	300	mV
Transition Frequency	f⊤	V _{CE} = 10V, I _E = -5mA	-	250	-	MHz
Input Resistance	R1	-	7	10	13	Kohm





Application Information

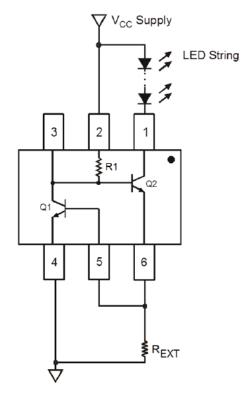
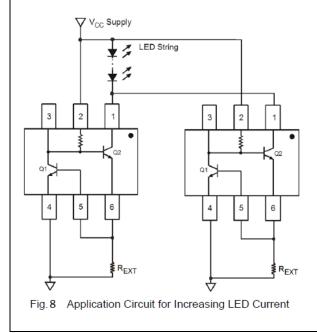


Fig. 7 Typical Application Circuit for Linear Mode Current Sink LED Driver



The 2sC164s has been designed for driving low current LEDs with typical LED current of 20mA to 100mA. It provides a cost effective way for driving low current LEDs compared with more complex switching regulator solutions. Furthermore, it reduces the PCB board area of the solution as there is no need for external components like inductors, capacitors and switching diodes.

Figure 7 shows a typical application circuit diagram for driving an LED or string of LEDs. The NPN transistor Q1 measures the LED current by sensing the voltage across an external resistor R_{EXT}. Q1 uses its V_{BE} as reference to set the voltage across R_{EXT} and controls the base current into Q2. Q2 operates in linear mode to regulate the LED current. The LED current is

 $I_{LED} = V_{BE(Q1)} / R_{EXT}$

From this, for any required LED current the necessary external resistor R_{EXT} can be calculated from

 $R_{EXT} = V_{BE(Q1)} / I_{LED}$

Two or more 2SC164S can be connected in parallel to construct higher current LED strings as shown in Figure 8

Consideration of the expected linear mode power dissipation must be factored into the design, with respect to the 2SC164S's thermal resistance. The maximum voltage across the device can be calculated by taking the maximum supply voltage less the voltage across the LED string.

$$\begin{split} & \forall_{CE(Q2)} = \forall_{CC} - \forall_{LED} - \forall_{BE(Q1)} \\ & P_D = \forall_{CE(Q2)} * I_{LED} + (\forall_{CC} - \forall_{BE(Q2)} - \forall_{BE(Q1)})^2 / R_1 \end{split}$$

As the output current of 2SC164s increases, it is necessary to provide appropriate thermal relief to the device. The power dissipation supported by the device is dependent upon the PCB board material, the copper area and the ambient temperature. The maximum dissipation the device can handle is given by:

 $P_{D} = (T_{J(MAX)} - T_{A}) / R_{\theta JA}$

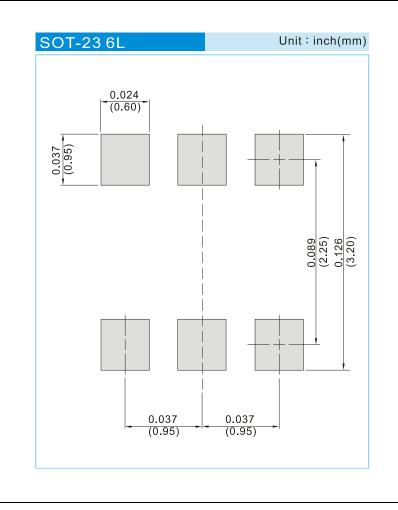




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version
2SC164S_R1_00001	SOT-23 6L	3K pcs / 7" reel	C4S	Halogen free
2SC164S_R2_00001	SOT-23 6L	10K pcs / 13" reel	C4S	Halogen free

MOUNTING PAD LAYOUT







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